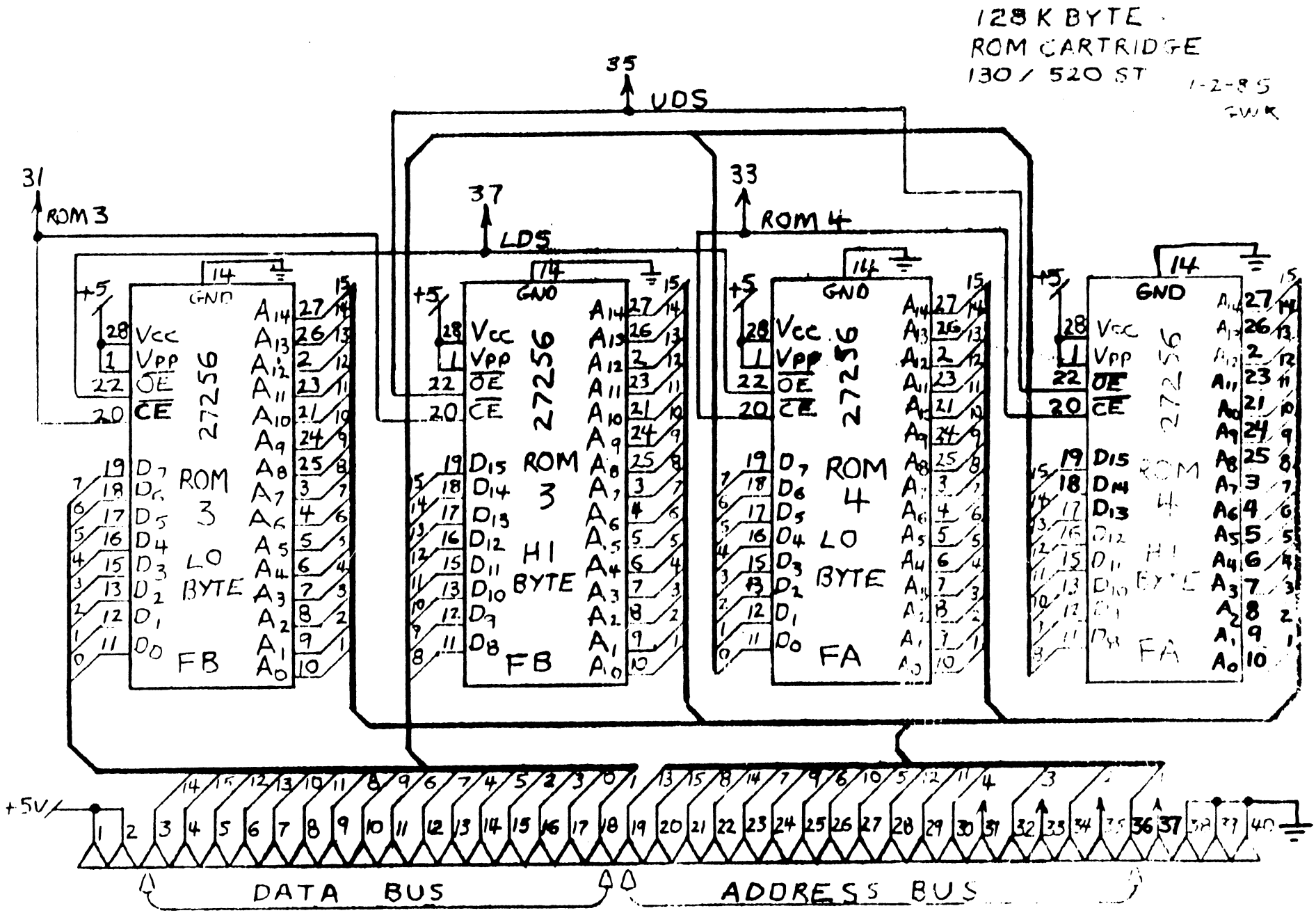
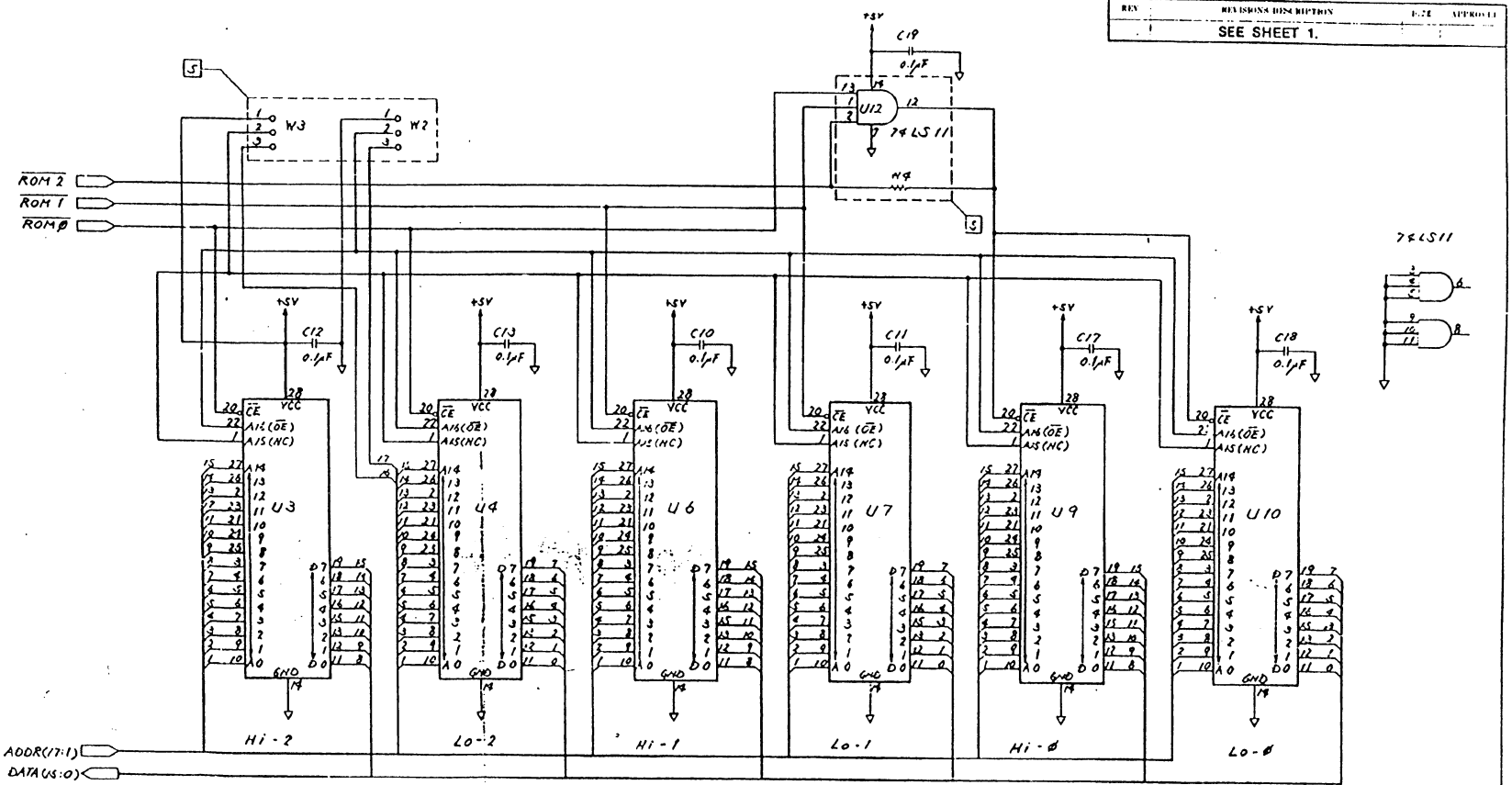

128K BYTE
ROM CARTRIDGE
130 / 520 ST
1-2-85
FWK
35
UDS
31
ROM 3
37
LDS
33
ROM 4
+5
GND
14
15
Vcc
Vpp
OE
CE
27256
ROM 3 LO BYTE FB
ROM 3 HI BYTE FB
ROM 4 LO BYTE FA
ROM 4 HI BYTE FA
+5V
DATA BUS
ADDRESS BUS

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



[5] ROM TABLE

IC ; U12 (74LS11)
JUMPER ; W2, W3, W4

ROM	LOCATION			
	W2	W3	W4	U12
256K	1-2	1-2	JUMPER	NO USE
1M x 2	2-3	2-3	NON	USE
[illegible]			JUMPER	

NOTES - UNLESS OTHERWISE SPECIFIED :

USED ON	DRAWN BY / DATE: NH 11-22-87	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Bukong 3F 4-3-10, Toranomon Minato-ku, Tokyo JAPAN	
NEXT ASSY	CHECKED / DATE: K.K. 12-1-87	TITLE: SCHEMATIC DIAGRAM MEGA MEGA 1	
MATERIAL	ENGINEER / DATE: 3-22-87 Kalama	SIZE: C	DRAWING NO.: C100166
FINISH	APPROVED / DATE: A.OKAZAKI 11/27/87	SCALE: NONE	SHEET 5 OF 6
	APPROVED / DATE: 12-1-87		